

**Workshop on Exascale Technologies 2016
AGENDA**

Speaker	Institution	Title	Time
Wednesday		January 27	
		Breakfast	7:45 AM
William Gropp	Illinois	Welcome and Charter	8:30 AM
Dave Richards	LLNL	Perspectives on Research and Production Tool Use from an Application Developer	8:45 AM
Gabe Rockefeller	LANL	Present Use and Future Needs for Tools in Integrated Code Development	9:00 AM
Mike Glass and Robert Hoekstra	SNL	Experience with Tools for Next Generation Platforms	9:15 AM
Martin Schulz	LLNL	Production and Research Development Tools for Current and Future Platforms	9:30 AM
Sam Gutierrez	LANL	Gladius: A Framework for Online Distributed Heap Analysis	9:45 AM
		BREAK	10:00 AM
Scott Pakin	LANL	Optimizing Applications for Future Hardware with Byfl	10:30 AM
David Boehme	LLNL	Caliper: Enabling Performance Annotations for Context_Aware Performance Analysis	10:45 AM
Sanjay Ranka	Florida	Autotuning and Multiobjective optimization for CMT NEK	11:00 AM
Carter Edwards	SNL	Kokkos: Performance portability for next generation HPC	11:15 AM
Alex Aiken	Stanford	Legion: Mapping and Analysis Tools	11:30 AM
Andreas Kloekner	Illinois	Loopy	11:45 AM
		Lunch	12:00 PM
Andrew Lumsdaine	Indiana	The Matrix Template Library: A generic programming approach to scalable linear algebra libraries	1:00 PM
James Sutherland	Utah	Spatial Ops/Nebo: A Domain-Specific Language for Portable Solution of PDEs on Structured Meshes	1:15 PM
Lawrence Rauchwerger	Texas A&M	TBA	1:30 PM
William Gropp		Description of breakout charge	1:45 PM
		Breakouts (5 rooms available)	2:00 PM
		BREAK	3:00 PM
		Breakouts continue	3:30 PM
		Breakout report	4:30 PM
		Adjourn for the day	5:00 PM
Thursday		28-Jan	
		Breakfast	7:45 AM
Arun Rodrigues	SNL	Performance Analysis and HPC System Simulation Tools	8:30 AM
Abhinav Bhatle	LLNL	Analyzing HPC Interconnects using Network Simulators	8:45 AM
Todd Gamblin	LLNL	Tools for Monitoring Entire HPC Centers: the Sonar Project at LLNL	9:00 AM
Sam White	Illinois	Charm++ & Adaptive MPI	9:15 AM
Jeremy Wilke	SNL	DHARMA: Distributed asynchRonomous Adaptive and Resilient Models for Applications	9:30 AM
Abhishek Kulkarni	Indiana	HPX-5: An experimental platform for exascale runtime system research	9:45 AM
		BREAK	10:00 AM
Tarun Prabhu	Illinois	Moya: A JIT compiler for HPC	10:30 AM
Kathryn Mohror	LLNL	Scalable Checkpoint / Restart Library (SCR)	10:45 AM
Sam Gutierrez	LANL	QUO: Accommodating Thread-Level Heterogeneity in Coupled MPI Applications	11:00 AM
Stephen Olivier	SNL	Qthreads: A library for lightweight threading	11:15 AM
Dong Ahn	LLNL	HPC Debugging Made Easy: LLNL's Debugging Tools Strategy	11:30 AM
Todd Gamblin	LLNL	Spack: A Tool for Managing Exascale Software	11:45 AM
		Lunch	12:00 PM
		Breakouts (5 rooms available)	1:00 PM
		BREAK	3:00 PM
		Breakout report	3:30 PM
		Adjourn	4:30 PM